

INTERNATIONAL JOURNAL OF ENGINEERING SCIENCES & RESEARCH TECHNOLOGY

SIMULATION OF EXACT MAXIMUM POWER POINT TRACKING OF PARTIALLY SHADED PV STRING USING CURRENT EQUALIZATION THEORY

Chandni Yogeshkumar Joshi *, Hardik Jayeshkumar Padariya

* Assistant Professor, Electrical Engineering Department, KJIT, Vadodara, India

Electrical Supervisor, Rajpipla, India

ABSTRACT

Maximum power point tracking (MPPT) algorithms are among the most important research topic related to photovoltaic (PV) system. Conventional MPPT method give effective results and easy to perform under uniform irradiation condition. However, as more than one local maxima is formed under partial shaded conditions so these methods are not absolutely successful. So, output power becomes lower than the absolute maximum. Conventional MPPT is designed to track the MPP will be trapped at the local MPP. Therefore, one of the challenges is to continuously track the absolute MPP while the environmental factors such as solar irradiation, PV temperature and partial shaded condition are rapidly changing. Also, in these partially shaded conditions, series connection of PV modules gives lowest current in PV string. This reduces the output power of PV string. To achieve maximum output power , a converter is connected in parallel with each PV module provides the extra (equalization) current in parallel with the shaded module to make the string current equal to the maximum string current. The equalization current supports the PV module to have its voltage regulated at a value that corresponds to its MPP voltage. This technique is known as DMPPT (Distributed MPPT) method.

KEYWORDS: MPPT, current compensation, DMPPT, partial shading, photovoltaic module.

INTRODUCTION

The renewable energy has increased much attraction these days because it can be recycled. The solar energy can be transform in to electrical energy, more effective than other renewable sources. However, compare to hydro, wind, geothermal, the solar energy are not widely used worldwide, due to high initial cost of solar cell. So, it is essential to recover as much energy as possible [11]. Individually from the quickly decreasing reserves of fossil fuels in the world, another major factor working against fossil fuels is the pollution related with their combustion. Contrastingly, renewable energy sources are well-known to be much cleaner and produce energy without the harmful effects of pollution unlike their conventional equivalents [9].

The tapping of solar energy owes its origins to the British astronomer John Herschel who notably used a solar thermal collector box to cook food during an excursion to Africa. Solar energy can be utilized in two major ways. First, the captured heat can be used as solar thermal energy, with applications in space heating. Another alternative is the transformation of incident solar radiation to electrical energy, which is the most usable form of energy. This can be achieved with the help of solar photovoltaic cells or with concentrating solar power plants [9].

However, the generation of electric power with the photovoltaic (PV) array is a method of "direct energy conversion" then it does not include intermediate stages like some of the other popular non-conventional energy sources. The voltage and current generated by a single solar cell is of no practical meaning since it is very small. Hence PV cells are attached in series to form a module. These modules are connected in series and parallel to form a PV array. In terrestrial applications, there is a possibility of partial shading of the PV array due to shadows of buildings, trees and clouds. The non-shaded portions of a PV array will be illuminated more than the shaded regions of the array. The reducing of the intensity of solar illumination on the shaded PV cells causes a decrease in power generation from these cells. Even a small shaded portion can cause a considerable reduction in the overall output power of the array. Whereas the short circuit current, reduces considerably during shading, the open circuit voltage does not change much.

http://www.ijesrt.com

© International Journal of Engineering Sciences & Research Technology

ISSN: 2277-9655 (I2OR), Publication Impact Factor: 3.785 (ISRA), Impact Factor: 2.114

A) Principle of Solar Cell

Solar cells comprise of a p-n junction fabricated in a thin layer of semiconductor. The semiconductor electrons can be placed in either the valence band or conduction band. Initially, all the electrons in the semiconductor fill up the valence band but when sunlight hits the semiconductor, some electrons gain enough energy to move from the valence band to the conduction band. The electrons in the conduction band then begin to move freely producing electricity. The electron leaving the valence band leaves a positively charged hole after and now that the valence band is no longer full, it aids the current flow. Most solar cells are doped to reduce the energy necessary for the electron to move from the valence band to the conduction band [10].

The amount of energy from sunlight, called photons, that is absorbed by a solar cell determines its efficiency. A photon can be reflected, absorbed or it can pass through a semiconductor. Since only the photons that are absorbed contribute to the electrical energy, it is significant to reduce the percentage of photons that pass through and that are reflected. An anti-reflective covering is usually applied to the surface of the solar cell to reduction the number of photons that are reflected. This reduces the percentage of photons that are reflected but some photons are still able to pass right through the semiconductor material [10].



B) Equivalent circuit of Solar cell

Fig. 2 shows the simplest equivalent circuit of a solar cell, which is a current source in parallel with a diode. The output of the current source is directly proportional to the light falling on the cell. The diode determines the I-V characteristics of the cell [9].



Fig. 2: circuit of solar cell

The equations which describe the I-V characteristics of the cell are

The output current from the photovoltaic array is

 $I_L = I_{PS} - I_d$

 $I_d = I_o (e^{qVd/kT} - 1)$

Where, Io is the reverse saturation current of the diode and its depends on temperature,

(1)

(2)

(3)

(4)

I_{PS} is the photo current and it's also depends on temperature,

 I_L is the load current,

q is the electron charge $(1.6 * 10^{-19} \text{C})$,

Vd is the voltage across the diode,

k is Boltzmann constant $(1.38 * 10^{-23} \text{ m}^2\text{Kg/s}^2\text{K})$,

G is the irradiance and Ips photo current depends on it and

T is the junction temperature in Kelvin (K)

From above equation

 $I = I^{PS} - I_o (e^{qVd/kT} - 1)$

Using suitable approximations,

 $I = I_{PS} - I_o \left(e^{q((V+ILRs)/nkT)} - 1 \right)$

Where, V is the PV cell voltage,

 R_s is the series resistance, which gives more accurate shape between the maximum power point and the open circuit voltage,

 R_P is the shunt resistance in parallel with the diode and n is the diode ideality factor [9].

C) Solar Characteristics

The I-V characteristics of a typical solar cell are as shown in the Fig. 3. When the voltage and the current characteristics are multiplied we get the P-V characteristics as shown in Fig. 4. The point indicated as MPP is the point at which the panel power output is maximum [9].



http://www.ijesrt.com

© International Journal of Engineering Sciences & Research Technology

[211]



Fig. 4: P-V Characteristics

MPPT TECHNIQUE FOR PARTIAL SHADING CONDITION

The attaching of solar energy using PV modules comes with its own problems that arise from the change in insolation conditions. These changes in insolation conditions severely affect the efficiency and output power of the PV modules. A great deal of research has been done to improve the efficiency of the PV modules. A number of methods of how to track the maximum power point of a PV module have been proposed [10].

A MPPT is used to extract maximum power from the solar PV module. And this power transfer to the load. Fig. 6 shows the basic block diagram of MPPT. A DC-DC converter (step up/step down) is used to transfer maximum power from solar PV module to load. A DC-DC converter acts as interface between the load and solar PV module. By changing the duty cycle the load impedance as seen by the source is varied and matched at the peak point of the peak power with the source so as to transfer the maximum power [5].

There are various methods to track maximum power of PV module, which is described below:

- i. Perturb and Observe (P&O)
- ii. Incremental Conductance (IC)
- iii. Fuzzy Logic Control
- iv. Ripple correlation control
- v. Peak current control



Fig. 5: Basic Block Diagram of MPPT

(i) Perturb and Observe (P&O)



Fig. 6: Flow chart of P&O method

In this algorithm a slight perturbation is introduce. This perturbation causes the power of the solar module changes. If the power increases due to the perturbation then the perturbation is continued in that direction. After the peak power is reached the power at the next instant decreases and hence after that the perturbation reverses [6]. When the steady state is reached the algorithm oscillates around the peak point. In order to keep the power variation small the perturbation size is kept very small. A PI controller then acts moving the operating point of the module to that particular voltage level. It is observed that, there some power loss due to this perturbation also the fails to track the power under fast varying atmospheric conditions. But still this algorithm is very popular, simple and easy to implement [6].

(ii) Incremental Conductance

The disadvantage of the perturb and observe method to track the peak power under fast varying atmospheric condition is overcome by IC method. The IC method can determine that the MPPT has reached the MPP and stop perturbing the operating point. If this condition is not met, the direction in which the MPPT operating point must be perturbed can be calculated using the relationship between dI/dV and –I/V. This relationship is derived from the fact that dP/dV is negative when the MPPT is to the right of the MPP and positive when it is to the left of the MPP [6].



EXTRACTION OF MAXIMUM POWER DURING PARTIAL SHADING CONDITION

A) Use of bypass diodes in parallel with the PV modules With bypass diodes, shaded PV modules are completely bypassed such that the un-shaded modules in the string can continue to provide power. This strategy increases the overall power yield but leads to:



Fig. 9: PV module with bypass Diode

- (i) Complete loss of power from partially shaded module and
- (ii) Appearance of multiple peaks in the power-voltage characteristics because of which MPPT becomes complex as shown in Fig. 4. Though, techniques for tracking the 'global' Maximum Power Point (GMPP) have been proposed for such conditions, they only provide approximate solutions [3].

B) Distributed MPPT

Mismatch in the characteristics of PV modules due to non-uniform solar radiation, temperature and manufacturing inequalities leads to a significant power loss in PV systems. When subjected to partial shading conditions, various PV modules of an n module string tend to generate different currents (I PVn). But PV modules connected in series must carry the same current, which should correspond to the maximum current (Istr-max) generated by any PV module of the string [3].

To ensure this, a DC-DC converter is connected in parallel to each PV module. The DC-DC converter derives its input power from the capacitor connected at the overall output of the PV array. For an n module string, the n str-max the converter provides the extra (equalization) current equal to (I) to make the string current equal to I str-max. The module voltage is appropriately regulated to a value corresponding to its MPP (Maximum Power Point) voltage with the DC-DC converter while providing the necessary equalization current as shown in Fig. 10. This scheme has the following advantages:

- (iii) It does not result in the formation of multiple peaks (as with the use of bypass diode) in the power-voltage characteristics of the overall array output, thus enabling the conventional MPPT algorithms (e.g. hill climbing method) to work satisfactorily.
- (iv) More efficient because each DC-DC converter supplies only the balance (equalization) current, thereby resulting in negligible losses [4].



Fig. 10: Distributed MPPT

C) Flyback converter as DC-DC converter

Fig. 11 shows the typical diagram of flyback converter. As may be seen from the circuit diagram of Fig. 11, when switch 'S' is on, the primary winding of the transformer gets connected to the input supply with its dotted end connected to the positive side. At this time the diode 'D' connected in series with the secondary winding gets reverse biased due to the induced voltage in the secondary (dotted end potential being higher). Thus with the turning on of switch 'S', primary winding is able to carry current but current in the secondary winding is blocked due to the reverse biased diode [14].

Mode-1: The flux established in the transformer core and linking the windings is entirely due to the primary winding current. This mode of circuit has been described here as Mode-1 of circuit operation. Fig. 12 shows (in bold line) the current carrying part of the circuit and Fig. 13 shows the circuit that is functionally equivalent to the fly-back circuit during mode-1. In the equivalent circuit shown, the conducting switch or diode is taken as a shorted switch and the device that is not conducting is taken as an open switch. This representation of switch is in line with our assumption where the switches and diodes are assumed to have ideal nature, having zero voltage drops during conduction and zero leakage current during off state [14].



Fig. 11: Flyback Converter



Fig. 12: Current path during Mode-1 of circuit operation

Mode-2: Mode-2 of circuit operation starts when switch 'S' is turned off after conducting for some time. The primary winding current path is broken and according to laws of magnetic induction, the voltage polarities across the windings reverse. Reversal of voltage polarities makes the diode in the secondary circuit forward biased. Fig. 14 shows the current path (in bold line) during mode-2 of circuit operation while Fig. 15 shows the functional equivalent of the circuit during this mode [14].



Fig. 13: Equivalent circuit in Mode-1



Fig. 14: Current path during Mode-2 of circuit operation



Fig. 15: Equivalent circuit in Mode-2

http://www.ijesrt.com

© International Journal of Engineering Sciences & Research Technology

ISSN: 2277-9655 (I2OR), Publication Impact Factor: 3.785 (ISRA), Impact Factor: 2.114

Fig. 16 shows the waveforms of flyback converter. In the figure, first waveform shows the primary winding current. It increases when switch on and decreases when switch off. Second waveform shows the secondary winding current. It increases when switch off and decreases when switch on. Third waveform shows the voltage across primary winding. It is positive when switch on and negative when switch off. At last fourth waveform shows the output voltage which is constant [13].



Fig. 16: Fly-back waveforms

SIMULATION AND RESULT

Open circuit voltage	21 (V)
Short circuit current	3.7 (A)
Voltage at maximum power	17 (V)
Current at maximum power	3.5 (A)
Maximum power	60 (W)

A) Simulation of PV model



Fig. 17: Simulation of PV model



Fig. 18: I-V curve of PV model



Fig. 19: P-V curve of PV model

http://www.ijesrt.com

© International Journal of Engineering Sciences & Research Technology [219] **B)** Simulation result with different irradiance



Fig. 20: I-V curve of PV model with different irradiance



Fig. 21: P-V curve of PV model with different irradiance Table 2. P-V Power and P-V Current at different irradiance

Irradiance Level	Power (Watt)	Current (Amp)
1	60	3.8
0.75	45	2.85
0.5	30	1.9
0.25	15	0.95

C) Simulation of PV with P&O MPPT method



Fig. 22: P&O MPPT method for varying Irradiation



Fig. 23: Sub system of P&O method Series connected PV cell with bypass diode



Fig. 24: Simulation of series connected PV cell © International Journal of Engineering Sciences & Research Technology

http://www.ijesrt.com



Fig. 25: Output current and power of series connected PV cell



Fig. 26: Output voltage of series connected PV cell



Fig. 27: Power V/S Voltage graph

Flyback converter as DC-DC converter



Fig. 28: Sub of Flyback converter as DC-DC converter

http://www.ijesrt.com

© International Journal of Engineering Sciences & Research Technology [222]

D) Distributed MPPT Simulation



Fig. 29: DMPPT model with three PV modules



Fig. 30: Varying irradiance on module 1





manor - Herbelsterk Willia	phillionna and a shaked

Fig. 32: Total Voltage

http://www.ijesrt.com

© International Journal of Engineering Sciences & Research Technology [223]

ISSN: 2277-9655 (I2OR), Publication Impact Factor: 3.785 (ISRA), Impact Factor: 2.114

	PT OTEN (11/10/1
- panet - Nelshinklin billin		phillip	
- [
-			
Luna			
	a		12

Fig. 33: Total Power







Fig. 35: PV module 2, Flyback 2 and Total Current



Fig. 36: PV module 3, Flyback 3 and Total Current

CONCLUSION

From the simulation, it can be concluded, the disadvantage of bypass diode is overcome by the use of DMPPT technique in partially shading condition. In this technique in partial shading condition, PV modules which are under shadow, is fed by the flyback converter. Flyback converter feeds balance current in to those shaded modules. The resultant power does not reduce and it does not result in the formation of multiple-peaks. So, from the simulation it can be concluded that under partially shading condition, DMPPT gives better output and work efficiently.

REFERENCES

- [1] Geoff Walker, "EVALUATING MPPT CONVERTER TOPOLOGIES USING A MATLAB PV MODEL", Dept of Computer Science and Electrical Engineering.
- [2] Weidong Xiao, Member, IEEE, Fonkwe Fongang Edwin, Student Member, IEEE, Giovanni Spagnuolo, Member, IEEE, and Juri Jatskevich, Senior Member, IEEE, "Efficient Approaches for Modeling and Simulating Photovoltaic Power Systems" IEEE JOURNAL OF PHOTOVOLTAICS, VOL. 3, NO. 1, JANUARY 2013.
- [3] Pooja Sharma, Student member, IEEE, and Vivek Agarwa, Senior member, IEEE, "Exact Maximum Power Point Tracking of Grid Connected Partially Shaded PV Source Using Current Compensation Concept", TPEL-Reg-2013-04-0575.
- [4] Pooja Sharma, Pradeep K. Peter, and Vivek Agarwal, Senior member, IEEE, "Exact Maximum Power Point Tracking of Partially Shaded PV Strings Based on Current Equalization Concept", 978-1-4673-0066-7/12/\$26.00 ©2011 IEEE.
- [5] A.Pradeep Kumar Yadav and S.Thirumaliah and G.Haritha, "Comparison of MPPT Algorithms for DC-DC Converters Based PV Systems", International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, pp. 18-23, July 2012.
- [6] Hairul Nissah Zainudin and Saad Mekhilef, "Comparison Study of Maximum Power Point Tracker Techniques for PV Systems", International Middle East Power Systems Conference, pp. 750-755, December 2010.
- [7] Jay Patel, Vishal sheth and Gaurang Sharma, "DESIGN & SIMULATION OF PHOTOVOLTAIC SYSTEM USING INCREMENTAL MPPT ALGORITHUM", International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, pp. 1647-1653, 2013.
- [8] D. P. Hohm and M. E. Ropp, "Comparative Study of Maximum Power Point Tracking Algorithms", PROGRESS IN PHOTOVOLTAICS: RESEARCH AND APPLICATIONS, John Wiley & Sons, Ltd., 2002.
- [9] Arjav Harjai, Abhishek Bhardwaj and Mrutyunjaya Sandhibigraha, "STUDY OF MAXIMUM POWER POINT TRACKING (MPPT) TECHNIQUES IN A SOLAR PHOTOVOLTAIC ARRAY", Department of Electrical Engineering, National Institute of Technology, Rourkela.

http://www.ijesrt.com

© International Journal of Engineering Sciences & Research Technology

- [10] SIMULATION OF PV ARRAY WITH BOOST CONVERTER: AN OPEN LOOP STUDY", Department DEBASHIS DAS and SHISHIR KUMAR PRADHAN, "MODELING AND of Electrical Engineering, National Institute of Technology, Rourkela.
- [11] Adedamola Omole, "Analysis, Modeling and Simulation of Optimal Power Tracking of
- [12] Multiple-Modules of Paralleled Solar Cell Systems", THE FLORIDA STATE UNIVERSITY COLLEGE OF ENGINEERING.
- [13] Muhammad H. Rashid, Power Electronics Circuits, Devices and Applications, 3rd Edition, Pearson Education, Inc, 2004.
- [14] Power Electronics Lecture_module 3 (DC to DC converters) Lesson 22 (Flyback type switched mode power

supply).http://nptel.ac.in/courses/Webcoursecontents/IIT%20Kharagpur/Power%20Electronics/PDF/L22%28DP%29%28PE%29%20%28%28EE%29NPTEL%29.pdf

[15] PV education site

Author Biblography

	Chandni Yogeshkumar Joshi
	received the B.E. in Electrical
1 month	Engineering from Vidhyabharti
	Trust Inst. of Technology &
	Research, Bardoli in 2012. She
100 10 10 C	received the M.E. in Electrical
North Alexan	Engineering from Parul Institute
ABBE NOBEL	of Technology, Vadodara in 2014.
	Presently Working as Asst. Proff.
	in Electrical Department, K J
	Institute of Technology, Savli. Her
	research interests include
	Renewable Energy Sources and
	Power Electronics.
	Hardik Jayeshkumar Padariya
	received the B.E. in Electrical
	Engineering from C.K.Pithawalla
la al	College of Engg. & Tech., Surat in
125	2012. He received the M.E. in
alte	Electrical Engineering from Parul
	Institute of Engg. & Tech.,
R.M.S. (Server	Vadodara in 2014. Presently
	working as Elecrtical Supervisor,
	Rajpipla. His research interests
	include Renewable Energy
	Sources, Power Electronics and
	Power System.

http://www.ijesrt.com